

PowerOn reset RTC=0

nRESET_PIC2GPS = active

LC:State:AutoNode:SystemStartup

AutoNodeRestartAction

jacinto_reset

----- VP2 HWE D1 -----

--UA--

TestPoint:0x0000FFFF

RESF::08=>PowerOn

Leaving StopMode,IGN active,Interrupt:ONOFF

WakeupReason:0x80080000

User_Apl:10140000

SWL_Apl:10140000

Date:16:36:00Jul 13 2015

getEventStatus: eEvt=2

MSA_StateHandler: eState=0 UBatt=1198

BS V2.4.1 -> BL:0001 >BL

LC:ev=28

Dirana_reset

LC:NOBS:AutoNode:BL Started

LC:NOBS[0]:NEWTIME(0->10)

LC:ev=21

Jac-BL v3.13.4-1.36:Appl,19,8064000 >BL

LC:NOBS:AutoNode:BL Finished

LC:NOBS[0]:NEWTIME(10->30)

LC:ev=23

LC:State:AutoNode:MonitorOS:Start

LC:State:AutoNode:Switching on CID

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,40,08,01,00,01,90,00,01,00,00,d9,00,

SPI : TX: aa,55,80,00,80,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: aa,55,50,0e,01,00,01,90,00,0c,00,06,f1,17,f1,18,06,aa,67,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,82,00,82,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,48,0d,01,30,f1,17,25,00,00,05,08,00,00,00,00,ba,

SPI : TX: aa,55,81,00,81,00,

SPI : RX: 00,00,00,00,00,00,

LC:System:AutoNode:Startup Achieved

LC:ev=14

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,50,0b,01,30,f1,17,00,10,00,03,00,01,30,ae,

SPI : TX: aa,55,82,00,82,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: aa,55,58,0a,01,30,f1,17,24,1c,00,02,00,00,bf,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,83,00,83,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,60,0a,01,30,f1,17,27,0c,00,02,02,02,94,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : RX: aa,55,58,08,01,30,f1,18,23,11,00,00,ba,00,
SPI : TX: aa,55,83,00,83,00,
SPI : RX: 00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,84,00,84,00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,68,0c,01,30,f1,17,28,1c,00,04,11,01,80,00,13,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,85,00,85,00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,70,10,01,30,f1,17,30,4c,00,08,00,00,00,00,00,00,c3,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : RX: aa,55,60,0d,01,30,f1,18,22,00,00,05,28,40,02,ff,00,07,
SPI : TX: aa,55,84,00,84,00,
SPI : RX: 00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,86,00,86,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,78,09,01,30,f1,18,23,1f,00,01,03,97,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,87,00,87,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,68,0b,01,30,f1,18,22,10,00,03,02,00,00,88,

SPI : TX: aa,55,85,00,85,00,

SPI : RX: 00,00,00,00,00,00,00,

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,70,11,01,30,f1,18,00,10,00,09,01,01,30,02,10,02,11,02,22,b1,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,00,

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0b,01,7b,02,0f,04,00,00,00,00,06,00,26,b6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a7,03,27,04,00,04,00,04,00,04,00,d9,00,

SPI : RX: aa,55,78,0b,01,30,f1,17,42,00,00,03,05,06,00,e6,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,87,00,87,00,

SPI : RX: 00,00,00,00,00,00,00,

SPI : TX: aa,55,40,09,01,30,f1,18,22,2c,00,01,00,9e,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,80,00,80,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,40,0a,01,30,f1,17,42,01,00,02,05,06,df,00,

SPI : TX: aa,55,80,00,80,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: aa,55,48,0c,01,30,f1,17,42,0c,00,04,05,06,01,00,db,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,81,00,81,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,48,0c,01,30,f1,17,29,00,00,04,00,00,0f,04,b5,00,

SPI : TX: aa,55,81,00,81,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Started

LC:NOBS[0]:NEWTIME(30->15)

LC:ev=24

LC:State:AutoNode:Operable

SPI : TX: aa,55,50,0c,01,30,f1,17,28,1c,00,04,11,01,80,08,23,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,82,00,82,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,50,0c,01,30,f1,17,29,00,00,04,00,00,0f,00,a9,00,

SPI : TX: aa,55,82,00,82,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:New Registration

LC:NOBS[0]:NEWREG(15)

LC:ev=25

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,58,0b,01,30,f1,17,42,00,00,03,05,06,00,c6,

SPI : TX: aa,55,83,00,83,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,60,0a,01,30,f1,17,42,01,00,02,05,06,ff,00,

SPI : TX: aa,55,84,00,84,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: aa,55,58,0c,01,30,f1,17,42,0c,00,04,05,06,01,00,cb,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,83,00,83,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,68,0a,01,30,f1,18,30,80,00,02,01,28,21,00,

SPI : TX: aa,55,85,00,85,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,70,0b,01,30,f1,17,42,00,00,03,08,04,01,e0,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,00,

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,78,0a,01,30,f1,17,42,01,00,02,08,04,e8,00,

SPI : TX: aa,55,87,00,87,00,

SPI : RX: 00,00,00,00,00,00,00,

SPI : TX: aa,55,60,10,00,00,f1,18,30,9c,00,08,00,00,00,00,00,00,00,3d,00,

SPI : RX: 00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,84,00,84,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,68,0c,01,30,f1,17,42,0c,00,04,08,04,01,01,f5,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,85,00,85,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,40,09,01,30,f1,17,41,01,00,01,14,cb,

SPI : TX: aa,55,80,00,80,00,

SPI : RX: 00,00,00,00,00,00,00,

SPI : TX: aa,55,70,0b,01,30,f1,17,41,0c,00,03,14,00,26,d0,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,86,00,86,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : RX: aa,55,48,0b,01,30,f1,17,42,00,00,03,08,07,00,da,
SPI : TX: aa,55,81,00,81,00,
SPI : RX: 00,00,00,00,00,00,
SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : RX: aa,55,50,0a,01,30,f1,17,42,01,00,02,08,07,c3,00,
SPI : TX: aa,55,82,00,82,00,
SPI : RX: 00,00,00,00,00,00,
SPI : TX: aa,55,78,0c,01,30,f1,17,42,0c,00,04,08,07,01,00,e7,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,87,00,87,00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : RX: aa,55,58,0b,01,30,f1,17,42,00,00,03,08,05,01,c9,
SPI : TX: aa,55,83,00,83,00,
SPI : RX: 00,00,00,00,00,00,
SPI : TX: aa,55,40,0c,01,30,f1,17,28,1c,00,04,11,05,80,08,37,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,80,00,80,00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : RX: aa,55,60,0a,01,30,f1,17,42,01,00,02,08,05,f1,00,
SPI : TX: aa,55,84,00,84,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: aa,55,48,0c,01,30,f1,17,42,0c,00,04,08,05,01,01,d4,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,81,00,81,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0c,01,7b,02,0f,04,00,00,00,00,06,00,26,b1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a7,03,27,04,00,04,00,04,00,04,00,d9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0d,01,7b,02,10,04,00,00,01,00,06,00,26,ae,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a7,03,27,04,00,04,00,04,00,04,00,d9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,68,12,01,30,f1,18,31,00,00,0a,00,00,00,00,00,00,00,00,00,99,00,

SPI : TX: aa,55,85,00,85,00,

SPI : RX: 00,00,00,00,00,00,

PWM[18]=0x0000

PWM[19]=0x0000

PWM[20]=0x0000

PWM[21]=0x0000

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,70,0b,01,30,f1,18,22,10,00,03,02,00,00,90,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: 00,

SPI : RX: aa,55,78,12,01,30,f1,18,31,00,00,0a,00,00,00,00,00,00,00,00,89,00,

SPI : TX: aa,55,87,00,87,00,

SPI : RX: 00,00,00,00,00,00,

PWM[18]=0x0000

PWM[19]=0x0000

PWM[20]=0x0000

PWM[21]=0x0000

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0d,01,7b,02,10,04,00,00,01,00,06,00,26,ae,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a8,03,27,04,00,04,00,04,00,04,00,d6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,50,09,01,30,f1,18,22,2c,00,01,00,8e,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,82,00,82,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0b,01,7b,02,0f,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a8,03,27,04,00,04,00,04,00,04,00,d6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,40,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b8,00,

SPI : TX: aa,55,80,00,80,00,

SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0c,01,7b,02,0f,04,00,00,01,00,06,00,26,b0,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a8,03,27,04,00,04,00,04,00,04,00,d6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0c,01,7b,02,10,04,00,00,01,00,06,00,26,af,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a8,03,27,04,00,04,00,04,00,04,00,d6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0c,01,7b,02,0f,04,00,00,01,00,06,00,26,b0,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a8,03,27,04,00,04,00,04,00,04,00,d6,00,

[illegible]

*FAN2PIC=0

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a8,03,27,04,00,04,00,04,00,04,00,d6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0d,04,00,00,01,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a8,03,27,04,00,04,00,04,00,04,00,d6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,60,10,00,00,f1,18,30,9c,00,08,00,00,00,00,00,00,00,08,35,00,

SPI : RX: 00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,84,00,84,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,68,10,00,00,f1,18,30,9c,00,08,00,00,00,00,00,00,00,00,35,00,

SPI : RX: 00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,85,00,85,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,50,0b,01,30,f1,17,42,00,00,03,05,06,01,cf,

SPI : TX: aa,55,82,00,82,00,
SPI : RX: 00,00,00,00,00,00,
SPI : TX: aa,55,70,0c,01,30,f1,17,28,1c,00,04,11,05,80,00,0f,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,86,00,86,00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : RX: aa,55,58,0a,01,30,f1,17,42,01,00,02,05,06,c7,00,
SPI : TX: aa,55,83,00,83,00,
SPI : RX: 00,00,00,00,00,00,
SPI : TX: aa,55,78,0c,01,30,f1,17,42,0c,00,04,05,06,01,01,ea,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,87,00,87,00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,
SPI : RX: aa,55,60,12,01,30,f1,18,31,00,00,0a,0f,ff,0c,7a,0d,1d,0c,cc,00,00,c7,00,
SPI : TX: aa,55,84,00,84,00,
SPI : RX: 00,00,00,00,00,00,
PWM[18]=0x0FFF
PWM[19]=0x0C7A
PWM[20]=0x0D1D
PWM[21]=0x0CCC
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a8,03,27,04,00,04,00,04,00,04,00,d6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,04,01,7c,02,09,04,00,00,01,00,06,00,26,b9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a8,03,27,04,00,04,00,04,00,04,00,d6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a7,03,27,04,00,04,00,04,00,04,00,d9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,48,09,01,30,f1,18,22,2c,00,01,00,96,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,81,00,81,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,70,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,88,00,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a7,03,27,04,00,04,00,04,00,04,00,d9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,06,01,7b,02,0a,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a7,03,26,04,00,04,00,04,00,04,00,d8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a7,03,26,04,00,04,00,04,00,04,00,d8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7c,02,09,04,00,00,01,00,06,00,26,b8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a7,03,26,04,00,04,00,04,00,04,00,d8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,50,09,01,30,f1,18,22,2c,00,01,00,8e,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,82,00,82,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,78,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,80,00,

SPI : TX: aa,55,87,00,87,00,

SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a7,03,25,04,00,04,00,04,00,04,00,db,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a7,03,25,04,00,04,00,04,00,04,00,db,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a5,03,a6,03,24,04,00,04,00,04,00,04,00,d8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a6,03,23,04,00,04,00,04,00,04,00,dc,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,04,01,7b,02,09,04,00,00,01,00,06,00,26,be,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a6,03,a6,03,23,04,00,04,00,04,00,04,00,dc,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a5,03,a6,03,22,04,00,04,00,04,00,04,00,de,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,60,09,01,30,f1,18,22,2c,00,01,00,be,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,84,00,84,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,48,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b0,00,

SPI : TX: aa,55,81,00,81,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a5,03,a6,03,22,04,00,04,00,04,00,04,00,de,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a5,03,a6,03,22,04,00,04,00,04,00,04,00,de,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,06,01,7b,02,0a,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a5,03,a6,03,21,04,00,04,00,04,00,04,00,dd,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,06,01,7b,02,0a,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a5,03,a5,03,21,04,00,04,00,04,00,04,00,de,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
*FAN2PIC=0
w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0
SPI : TX: aa,55,68,09,01,30,f1,18,22,2c,00,01,00,b6,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,00,
SPI : RX: aa,55,85,00,85,00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,
SPI : RX: aa,55,50,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,a8,00,
SPI : TX: aa,55,82,00,82,00,
SPI : RX: 00,00,00,00,00,00,00,
LC:NOBS:AutoNode:OS Running
LC:ev=26
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,04,01,7b,02,08,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a5,03,a5,03,21,04,00,04,00,04,00,04,00,de,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,78,09,01,30,f1,18,22,2c,00,01,00,a6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,87,00,87,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,04,01,7b,02,08,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a5,03,a4,03,1e,04,00,04,00,04,00,04,00,e0,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,60,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,98,00,

SPI : TX: aa,55,84,00,84,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a4,03,a4,03,1e,04,00,04,00,04,00,04,00,e1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

```
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,0a,04,00,00,01,00,06,00,26,bc,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a4,03,a4,03,1e,04,00,04,00,04,00,04,00,e1,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,06,01,7b,02,0a,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a4,03,a4,03,1e,04,00,04,00,04,00,04,00,e1,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,40,09,01,30,f1,18,22,2c,00,01,00,9e,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,80,00,80,00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a4,03,a4,03,1d,04,00,04,00,04,00,04,00,e2,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
```

SPI : RX: aa,55,68,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,90,00,

SPI : TX: aa,55,85,00,85,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a4,03,a3,03,1d,04,00,04,00,04,00,04,00,e5,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a4,03,a3,03,1d,04,00,04,00,04,00,04,00,e5,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,03,01,7b,02,08,04,00,00,01,00,06,00,26,b8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a4,03,a3,03,1d,04,00,04,00,04,00,04,00,e5,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,48,09,01,30,f1,18,22,2c,00,01,00,96,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,81,00,81,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,03,01,7b,02,08,04,00,00,01,00,06,00,26,b8,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a4,03,a3,03,1d,04,00,04,00,04,00,04,00,e5,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,70,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,88,00,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a4,03,a3,03,1c,04,00,04,00,04,00,04,00,e4,00,

[illegible]

SPI : CLEAR TO SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a4,03,a2,03,1c,04,00,04,00,04,00,04,00,e5,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a4,03,a2,03,1c,04,00,04,00,04,00,04,00,e5,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,50,09,01,30,f1,18,22,2c,00,01,00,8e,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,82,00,82,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,04,01,7b,02,09,04,00,00,01,00,06,00,26,be,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a3,03,a2,03,1c,04,00,04,00,04,00,04,00,e2,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,78,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,80,00,

SPI : TX: aa,55,87,00,87,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a3,03,a2,03,1c,04,00,04,00,04,00,04,00,e2,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a3,03,a2,03,1b,04,00,04,00,04,00,04,00,e5,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a3,03,a2,03,1b,04,00,04,00,04,00,04,00,e5,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,58,09,01,30,f1,18,22,2c,00,01,00,86,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,83,00,83,00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7c,02,09,04,00,00,01,00,06,00,26,b8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a3,03,a2,03,1b,04,00,04,00,04,00,04,00,e5,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,40,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b8,00,

SPI : TX: aa,55,80,00,80,00,

SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a3,03,a2,03,1b,04,00,04,00,04,00,04,00,e5,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a3,03,a1,03,1b,04,00,04,00,04,00,04,00,e6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,04,01,7b,02,08,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a3,03,a1,03,1a,04,00,04,00,04,00,04,00,e7,00,

[illegible]

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,60,09,01,30,f1,18,22,2c,00,01,00,be,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,84,00,84,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,02,00,06,00,26,bc,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a3,03,a1,03,1a,04,00,04,00,04,00,04,00,e7,00,

[illegible]

SPI : CLEAR_TO_SEND

[illegible]

SPI : RX: aa,55,48,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b0,00,

SPI : TX: aa,55,81,00,81,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a2,03,a1,03,1a,04,00,04,00,04,00,04,00,e6,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a2,03,a1,03,1a,04,00,04,00,04,00,04,00,e6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a2,03,a1,03,1a,04,00,04,00,04,00,04,00,e6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,68,09,01,30,f1,18,22,2c,00,01,00,b6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,85,00,85,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a2,03,a1,03,1a,04,00,04,00,04,00,04,00,e6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,50,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,a8,00,

SPI : TX: aa,55,82,00,82,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a2,03,a0,03,1a,04,00,04,00,04,00,04,00,e7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a2,03,a0,03,1a,04,00,04,00,04,00,04,00,e7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a2,03,a0,03,19,04,00,04,00,04,00,04,00,e4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,70,09,01,30,f1,18,22,2c,00,01,00,ae,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,86,00,86,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a1,03,a0,03,19,04,00,04,00,04,00,04,00,e7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,58,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,a0,00,

SPI : TX: aa,55,83,00,83,00,

SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,02,00,06,00,26,bc,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a1,03,a0,03,19,04,00,04,00,04,00,04,00,e7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a1,03,a0,03,19,04,00,04,00,04,00,04,00,e7,00,

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a1,03,9f,03,19,04,00,04,00,04,00,04,00,d8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a1,03,9f,03,18,04,00,04,00,04,00,04,00,d9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7c,02,09,04,00,00,01,00,06,00,26,b8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a0,03,9f,03,18,04,00,04,00,04,00,04,00,d8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,40,09,01,30,f1,18,22,2c,00,01,00,9e,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,80,00,80,00,

SPI : CLEAR_TO_SEND

[illegible]

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a0,03,9f,03,18,04,00,04,00,04,00,04,00,d8,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
*FAN2PIC=0
w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0
SPI : TX: aa,55,48,09,01,30,f1,18,22,2c,00,01,00,96,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,00,
SPI : RX: aa,55,81,00,81,00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a0,03,9f,03,18,04,00,04,00,04,00,04,00,d8,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,
SPI : RX: aa,55,70,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,88,00,
SPI : TX: aa,55,86,00,86,00,
SPI : RX: 00,00,00,00,00,00,00,
LC:NOBS:AutoNode:OS Running
LC:ev=26
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,02,00,06,00,26,bc,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,a0,03,9f,03,17,04,00,04,00,04,00,04,00,d7,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,78,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,80,00,

SPI : TX: aa,55,87,00,87,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9f,03,9e,03,17,04,00,04,00,04,00,04,00,e9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9f,03,9e,03,17,04,00,04,00,04,00,04,00,e9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9f,03,9e,03,17,04,00,04,00,04,00,04,00,e9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,58,09,01,30,f1,18,22,2c,00,01,00,86,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,83,00,83,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9f,03,9e,03,16,04,00,04,00,04,00,04,00,e8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,40,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b8,00,

SPI : TX: aa,55,80,00,80,00,

SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9f,03,9e,03,16,04,00,04,00,04,00,04,00,e8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9f,03,9e,03,16,04,00,04,00,04,00,04,00,e8,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9f,03,9e,03,16,04,00,04,00,04,00,04,00,e8,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,60,09,01,30,f1,18,22,2c,00,01,00,be,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,
SPI : RX: aa,55,84,00,84,00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9f,03,9d,03,16,04,00,04,00,04,00,04,00,eb,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: 00,
SPI : RX: aa,55,48,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b0,00,
SPI : TX: aa,55,81,00,81,00,
SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9e,03,9d,03,16,04,00,04,00,04,00,04,00,ea,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9e,03,9d,03,16,04,00,04,00,04,00,04,00,ea,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,02,00,06,00,26,bc,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9e,03,9d,03,16,04,00,04,00,04,00,04,00,ea,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,68,09,01,30,f1,18,22,2c,00,01,00,b6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,85,00,85,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9e,03,9d,03,15,04,00,04,00,04,00,04,00,e9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,70,09,01,30,f1,18,22,2c,00,01,00,ae,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,86,00,86,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9e,03,9d,03,15,04,00,04,00,04,00,04,00,e9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,58,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,a0,00,

SPI : TX: aa,55,83,00,83,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9e,03,9d,03,15,04,00,04,00,04,00,04,00,e9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9e,03,9d,03,15,04,00,04,00,04,00,04,00,e9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9d,03,9c,03,15,04,00,04,00,04,00,04,00,eb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,78,09,01,30,f1,18,22,2c,00,01,00,a6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,87,00,87,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9d,03,9c,03,15,04,00,04,00,04,00,04,00,eb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,60,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,98,00,

SPI : TX: aa,55,84,00,84,00,

SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9d,03,9c,03,15,04,00,04,00,04,00,04,00,eb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9d,03,9c,03,14,04,00,04,00,04,00,04,00,ea,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9d,03,9c,03,14,04,00,04,00,04,00,04,00,ea,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9d,03,9c,03,14,04,00,04,00,04,00,04,00,ea,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,04,01,7b,02,09,04,00,00,01,00,06,00,26,be,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9d,03,9c,03,14,04,00,04,00,04,00,04,00,ea,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,48,09,01,30,f1,18,22,2c,00,01,00,96,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,81,00,81,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9d,03,9c,03,14,04,00,04,00,04,00,04,00,ea,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,70,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,88,00,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9c,03,9c,03,14,04,00,04,00,04,00,04,00,eb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9c,03,9b,03,14,04,00,04,00,04,00,04,00,ec,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9c,03,9b,03,13,04,00,04,00,04,00,04,00,eb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,50,09,01,30,f1,18,22,2c,00,01,00,8e,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,82,00,82,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9c,03,9b,03,13,04,00,04,00,04,00,04,00,eb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,78,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,80,00,

SPI : TX: aa,55,87,00,87,00,

SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9c,03,9b,03,13,04,00,04,00,04,00,04,00,eb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9c,03,9b,03,13,04,00,04,00,04,00,04,00,eb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9c,03,9b,03,13,04,00,04,00,04,00,04,00,eb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9c,03,9b,03,13,04,00,04,00,04,00,04,00,eb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9b,03,9b,03,13,04,00,04,00,04,00,04,00,ec,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,60,09,01,30,f1,18,22,2c,00,01,00,be,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,84,00,84,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

[illegible]

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,68,09,01,30,f1,18,22,2c,00,01,00,b6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,85,00,85,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,02,00,06,00,26,bc,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9b,03,9a,03,13,04,00,04,00,04,00,04,00,ed,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,50,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,a8,00,

SPI : TX: aa,55,82,00,82,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9a,03,9a,03,12,04,00,04,00,04,00,04,00,ed,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : TX: aa,55,83,00,83,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9a,03,9a,03,12,04,00,04,00,04,00,04,00,ed,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9a,03,9a,03,12,04,00,04,00,04,00,04,00,ed,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,9a,03,9a,03,12,04,00,04,00,04,00,04,00,ed,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,78,09,01,30,f1,18,22,2c,00,01,00,a6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,87,00,87,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,99,03,9a,03,12,04,00,04,00,04,00,04,00,ee,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,60,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,98,00,

SPI : TX: aa,55,84,00,84,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,99,03,99,03,12,04,00,04,00,04,00,04,00,ed,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,99,03,9a,03,12,04,00,04,00,04,00,04,00,ee,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,99,03,9a,03,12,04,00,04,00,04,00,04,00,ee,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
*FAN2PIC=0
w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0
SPI : TX: aa,55,40,09,01,30,f1,18,22,2c,00,01,00,9e,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : RX: aa,55,80,00,80,00,00,00,00,00,00,00,00,00,00,00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,02,00,06,00,26,bc,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,99,03,99,03,11,04,00,04,00,04,00,04,00,ee,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,
SPI : RX: aa,55,68,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,90,00,00,00,00,00,00,00,00,00,00,
SPI : TX: aa,55,85,00,85,00,
SPI : RX: 00,
LC:NOBS:AutoNode:OS Running
LC:ev=26
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,02,00,06,00,26,bc,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,99,03,99,03,11,04,00,04,00,04,00,04,00,ee,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,70,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,88,00,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,98,03,99,03,11,04,00,04,00,04,00,04,00,ef,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,98,03,99,03,11,04,00,04,00,04,00,04,00,ef,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

[illegible]

SPI : CLEAR TO SEND

SPI : TX: aa.55.00.16.01.30.f1.18.21.1c.00.0e.02.98.03.99.03.11.04.00.04.00.04.00.04.00.ef.00.

[illegible]

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,50,09,01,30,f1,18,22,2c,00,01,00,8e,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,82,00,82,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,98,03,99,03,11,04,00,04,00,04,00,04,00,ef,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,78,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,80,00,

SPI : TX: aa,55,87,00,87,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,98,03,99,03,11,04,00,04,00,04,00,04,00,ef,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,02,00,06,00,26,bc,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,98,03,99,03,11,04,00,04,00,04,00,04,00,ef,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,06,01,7b,02,09,04,00,00,01,00,06,00,26,bc,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,98,03,99,03,11,04,00,04,00,04,00,04,00,ef,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,58,09,01,30,f1,18,22,2c,00,01,00,86,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,83,00,83,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,97,03,99,03,11,04,00,04,00,04,00,04,00,e0,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: aa,55,84,00,84,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,04,01,7b,02,09,04,00,00,01,00,06,00,26,be,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,97,03,99,03,10,04,00,04,00,04,00,04,00,e1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,97,03,98,03,10,04,00,04,00,04,00,04,00,e0,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,48,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b0,00,

SPI : TX: aa,55,81,00,81,00,

SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,97,03,98,03,10,04,00,04,00,04,00,04,00,e0,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,04,01,7b,02,08,04,00,00,01,00,06,00,26,bf,00,

SPI : TX: aa,55,82,00,82,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,96,03,98,03,10,04,00,04,00,04,00,04,00,e1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,96,03,98,03,10,04,00,04,00,04,00,04,00,e1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,70,09,01,30,f1,18,22,2c,00,01,00,ae,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,86,00,86,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

[illegible]

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,78,09,01,30,f1,18,22,2c,00,01,00,a6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,87,00,87,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,96,03,98,03,0f,04,00,04,00,04,00,04,00,fe,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,95,03,98,03,0f,04,00,04,00,04,00,04,00,fd,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,60,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,98,00,

SPI : TX: aa,55,84,00,84,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,95,03,98,03,0f,04,00,04,00,04,00,04,00,fd,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,68,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,90,00,

SPI : TX: aa,55,85,00,85,00,

SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,95,03,98,03,0f,04,00,04,00,04,00,04,00,fd,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,95,03,97,03,0f,04,00,04,00,04,00,04,00,f2,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,48,09,01,30,f1,18,22,2c,00,01,00,96,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,81,00,81,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,94,03,97,03,0f,04,00,04,00,04,00,04,00,f3,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,94,03,97,03,0f,04,00,04,00,04,00,04,00,f3,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,70,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,88,00,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,94,03,97,03,0f,04,00,04,00,04,00,04,00,f3,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,94,03,97,03,0f,04,00,04,00,04,00,04,00,f3,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
*FAN2PIC=0
w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0
SPI : TX: aa,55,50,09,01,30,f1,18,22,2c,00,01,00,8e,
SPI : RX: 00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,
SPI : RX: aa,55,82,00,82,00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,05,01,7b,02,09,04,00,00,01,00,06,00,26,bf,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,94,03,97,03,0f,04,00,04,00,04,00,04,00,f3,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,
SPI : RX: aa,55,78,0d,01,30,f1,17,25,00,00,05,09,00,00,00,1c,97,
SPI : TX: aa,55,87,00,87,00,
SPI : RX: 00,
LC:State:AutoNode:Shutdown Request:Tout=28sec
LC:System:Restart request->NOBS,PicObsCtr_NodeHealthy= 1
LC:ev=15

SPI : TX: aa,55,58,0a,01,30,f1,17,27,0c,00,02,00,00,ac,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,83,00,83,00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,
SPI : RX: aa,55,40,12,01,30,f1,18,31,00,00,0a,00,00,00,00,00,00,00,00,b1,00,
SPI : TX: aa,55,80,00,80,00,
SPI : RX: 00,00,00,00,00,00,
PWM[18]=0x0000
PWM[19]=0x0000
PWM[20]=0x0000
PWM[21]=0x0000
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,94,03,97,03,0f,04,00,04,00,04,00,04,00,f3,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,93,03,97,03,0e,04,00,04,00,04,00,04,00,f5,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,60,0c,01,30,f1,17,28,1c,00,04,11,05,80,08,17,00,

SPI : RX: 00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,84,00,84,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,93,03,97,03,0e,04,00,04,00,04,00,04,00,f5,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,68,09,01,30,f1,18,22,2c,00,01,00,b6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,85,00,85,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,93,03,97,03,0e,04,00,04,00,04,00,04,00,f5,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,48,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b0,00,

SPI : TX: aa,55,81,00,81,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,09,01,7b,02,0e,04,00,00,01,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,93,03,97,03,0f,04,00,04,00,04,00,04,00,f4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0d,01,7b,02,11,04,00,00,01,00,06,00,26,af,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,93,03,97,03,0f,04,00,04,00,04,00,04,00,f4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0d,01,7b,02,11,04,00,00,02,00,06,00,26,ac,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,93,03,97,03,0f,04,00,04,00,04,00,04,00,f4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,50,0d,01,30,f1,17,25,00,00,05,07,00,00,00,00,ad,

SPI : TX: aa,55,82,00,82,00,

SPI : RX: 00,00,00,00,00,00,

LC:ev=16

LC:State:AutoNode:Shutdown:Achieved

AutoNodeRestartAction

jacinto_reset

LC:State:AutoNode:Restarting

LC:ev= 5

BS V2.4.1 -> BL:0001 >BL

LC:ev=28

LC:NOBS:AutoNode:BL Started

LC:NOBS[0]:NEWTIME(15->10)

LC:ev=21

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

Jac-BL v3.13.4-1.36:Appl,19,8064000 >BL

LC:NOBS:AutoNode:BL Finished

LC:NOBS[0]:NEWTIME(10->30)

LC:ev=23

LC:State:AutoNode:MonitorOS:Start

LC:State:AutoNode:Switching on CID

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,40,08,01,00,01,90,00,01,00,00,d9,00,

SPI : TX: aa,55,80,00,80,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: aa,55,78,0e,01,00,01,90,00,0c,00,06,f1,17,f1,18,06,aa,4f,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,87,00,87,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,48,0d,01,30,f1,17,25,00,00,05,08,00,00,00,00,ba,

SPI : TX: aa,55,81,00,81,00,

SPI : RX: 00,00,00,00,00,00,

LC:System:AutoNode:Startup Achieved

LC:ev=14

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,50,0b,01,30,f1,17,00,10,00,03,00,01,30,ae,

SPI : TX: aa,55,82,00,82,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: aa,55,40,0a,01,30,f1,17,24,1c,00,02,00,00,a7,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,80,00,80,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,48,0a,01,30,f1,17,27,0c,00,02,00,00,bc,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,58,08,01,30,f1,18,23,11,00,00,ba,00,

SPI : TX: aa,55,83,00,83,00,

SPI : RX: 00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,81,00,81,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,50,0c,01,30,f1,17,28,1c,00,04,11,05,80,00,2f,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,82,00,82,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,58,10,01,30,f1,17,30,4c,00,08,00,00,00,00,00,00,eb,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,60,0d,01,30,f1,18,22,00,00,05,28,40,02,ff,00,07,

SPI : TX: aa,55,84,00,84,00,

SPI : RX: 00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,83,00,83,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,60,09,01,30,f1,18,23,1f,00,01,03,8f,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,84,00,84,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,68,0b,01,30,f1,18,22,10,00,03,02,00,00,88,

SPI : TX: aa,55,85,00,85,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,70,11,01,30,f1,18,00,10,00,09,01,01,30,02,10,02,11,02,22,b1,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0c,01,7b,02,0f,04,00,00,01,00,06,00,26,b0,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,92,03,97,03,11,04,00,04,00,04,00,04,00,eb,00,

SPI : RX: aa,55,78,0b,01,30,f1,17,42,00,00,03,05,06,00,e6,00,00,00,00,00,00,00,00,00,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,87,00,87,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: aa,55,68,09,01,30,f1,18,22,2c,00,01,00,b6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,85,00,85,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,40,0a,01,30,f1,17,42,01,00,02,05,06,df,00,

SPI : TX: aa,55,80,00,80,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: aa,55,70,0c,01,30,f1,17,42,0c,00,04,05,06,01,00,e3,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,86,00,86,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,78,0c,01,30,f1,17,28,1c,00,04,11,05,80,08,0f,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,48,0c,01,30,f1,17,29,00,00,04,00,00,0f,04,b5,00,

SPI : TX: aa,55,81,00,81,00,

SPI : RX: 00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

LC:NOBS:AutoNode:OS Started

LC:NOBS[0]:NEWTIME(30->15)

LC:ev=24

LC:State:AutoNode:Operable

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,87,00,87,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0b,01,7b,02,0f,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,92,03,97,03,11,04,00,04,00,04,00,04,00,eb,00,

SPI : RX: aa,55,50,0c,01,30,f1,17,29,00,00,04,00,00,0f,00,a9,00,00,00,00,00,00,00,00,00,00,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,82,00,82,00,

SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:New Registration

LC:NOBS[0]:NEWREG(15)

LC:ev=25

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,58,0b,01,30,f1,17,42,00,00,03,05,06,00,c6,

SPI : TX: aa,55,83,00,83,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,60,0a,01,30,f1,17,42,01,00,02,05,06,ff,00,

SPI : TX: aa,55,84,00,84,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: aa,55,40,0c,01,30,f1,17,42,0c,00,04,05,06,01,00,d3,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,80,00,80,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,68,0a,01,30,f1,18,30,80,00,02,01,28,21,00,

SPI : TX: aa,55,85,00,85,00,

SPI : RX: 00,00,00,00,00,00,

SPI : TX: aa,55,48,10,00,00,f1,18,30,9c,00,08,00,00,00,00,00,00,15,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,70,0b,01,30,f1,17,42,00,00,03,08,04,01,e0,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,81,00,81,00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : RX: aa,55,78,0a,01,30,f1,17,42,01,00,02,08,04,e8,00,
SPI : TX: aa,55,87,00,87,00,
SPI : RX: 00,00,00,00,00,00,
SPI : TX: aa,55,50,0c,01,30,f1,17,42,0c,00,04,08,04,01,01,cd,00,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,82,00,82,00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : RX: aa,55,40,09,01,30,f1,17,41,01,00,01,14,cb,
SPI : TX: aa,55,80,00,80,00,
SPI : RX: 00,00,00,00,00,00,
SPI : TX: aa,55,58,0b,01,30,f1,17,41,0c,00,03,14,00,26,f8,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,83,00,83,00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : RX: aa,55,48,0b,01,30,f1,17,42,00,00,03,08,07,00,da,
SPI : TX: aa,55,81,00,81,00,
SPI : RX: 00,00,00,00,00,00,

[illegible]

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,92,03,97,03,11,04,00,04,00,04,00,04,00,eb,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0b,01,7b,02,0f,04,00,00,01,00,06,00,26,b7,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,92,03,97,03,11,04,00,04,00,04,00,04,00,eb,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,
SPI : RX: aa,55,68,12,01,30,f1,18,31,00,00,0a,00,00,00,00,00,00,00,00,00,00,99,00,
SPI : TX: aa,55,85,00,85,00,
SPI : RX: 00,00,00,00,00,00,00,
PWM[18]=0x0000
PWM[19]=0x0000
PWM[20]=0x0000
PWM[21]=0x0000
SPI : TX: 00,
SPI : RX: aa,55,70,0b,01,30,f1,18,22,10,00,03,02,00,00,90,
SPI : TX: aa,55,86,00,86,00,
SPI : RX: 00,00,00,00,00,00,00,
SPI : TX: 00,
SPI : RX: aa,55,78,12,01,30,f1,18,31,00,00,0a,00,00,00,00,00,00,00,00,00,00,89,00,
SPI : TX: aa,55,87,00,87,00,
SPI : RX: 00,00,00,00,00,00,00,
PWM[18]=0x0000
PWM[19]=0x0000

PWM[20]=0x0000

PWM[21]=0x0000

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0b,01,7b,02,0f,04,00,00,02,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,92,03,97,03,12,04,00,04,00,04,00,04,00,e8,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,70,09,01,30,f1,18,22,2c,00,01,00,ae,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,86,00,86,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0b,01,7b,02,0f,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,92,03,98,03,12,04,00,04,00,04,00,04,00,e7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,40,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b8,00,

SPI : TX: aa,55,80,00,80,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0b,01,7b,02,0f,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,92,03,97,03,13,04,00,04,00,04,00,04,00,e9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0b,01,7b,02,0f,04,00,00,02,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,92,03,98,03,13,04,00,04,00,04,00,04,00,e6,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0b,01,7b,02,0f,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,92,03,98,03,14,04,00,04,00,04,00,04,00,e1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,78,09,01,30,f1,18,22,2c,00,01,00,a6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,87,00,87,00,

SPI : CLEAR_TO_SEND

[illegible]

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,91,03,98,03,15,04,00,04,00,04,00,04,00,e3,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
*FAN2PIC=0
w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0
SPI : TX: aa,55,40,09,01,30,f1,18,22,2c,00,01,00,9e,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK
SPI : TX: 00,00,00,00,00,00,00,
SPI : RX: aa,55,80,00,80,00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,91,03,98,03,15,04,00,04,00,04,00,04,00,e3,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: 00,
SPI : RX: aa,55,50,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,a8,00,
SPI : TX: aa,55,82,00,82,00,
SPI : RX: 00,00,00,00,00,00,00,
LC:NOBS:AutoNode:OS Running
LC:ev=26
SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND
SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,91,03,98,03,16,04,00,04,00,04,00,04,00,e0,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,58,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,a0,00,

SPI : TX: aa,55,83,00,83,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,91,03,98,03,17,04,00,04,00,04,00,04,00,e1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,91,03,98,03,17,04,00,04,00,04,00,04,00,e1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,90,03,98,03,17,04,00,04,00,04,00,04,00,e0,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,50,09,01,30,f1,18,22,2c,00,01,00,8e,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,82,00,82,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,90,03,98,03,18,04,00,04,00,04,00,04,00,ef,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,60,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,98,00,

SPI : TX: aa,55,84,00,84,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,09,01,7b,02,0d,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,90,03,98,03,18,04,00,04,00,04,00,04,00,ef,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,09,01,7b,02,0d,04,00,00,02,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,90,03,98,03,18,04,00,04,00,04,00,04,00,ef,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,90,03,98,03,18,04,00,04,00,04,00,04,00,ef,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,58,09,01,30,f1,18,22,2c,00,01,00,86,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,83,00,83,00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,90,03,98,03,18,04,00,04,00,04,00,04,00,ef,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: 00,
SPI : RX: aa,55,68,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,90,00,
SPI : TX: aa,55,85,00,85,00,
SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0d,04,00,00,01,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,90,03,97,03,19,04,00,04,00,04,00,04,00,e1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,90,03,97,03,19,04,00,04,00,04,00,04,00,e1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,90,03,98,03,19,04,00,04,00,04,00,04,00,ee,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,60,09,01,30,f1,18,22,2c,00,01,00,be,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,84,00,84,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,90,03,98,03,19,04,00,04,00,04,00,04,00,ee,00,

[illegible]

SPI : CLEAR_TO_SEND

[illegible]

SPI : RX: aa,55,70,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,88,00,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,90,03,98,03,19,04,00,04,00,04,00,04,00,ee,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8f,03,98,03,1a,04,00,04,00,04,00,04,00,f2,00,

[illegible]

SPI : CLEAR TO SEND

SPI : TX: aa.55.00.16.01.30.f1.18.21.0c.00.0e.03.0a.01.7b.02.0e.04.00.00.02.00.06.00.26.b4.00.

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,90,03,98,03,1a,04,00,04,00,04,00,04,00,ed,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,68,09,01,30,f1,18,22,2c,00,01,00,b6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,85,00,85,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8f,03,98,03,1a,04,00,04,00,04,00,04,00,f2,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,78,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,80,00,

SPI : TX: aa,55,87,00,87,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,09,01,7b,02,0e,04,00,00,02,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8f,03,97,03,1a,04,00,04,00,04,00,04,00,fd,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0d,04,00,00,02,00,06,00,26,b7,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8f,03,98,03,1a,04,00,04,00,04,00,04,00,f2,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8f,03,97,03,1b,04,00,04,00,04,00,04,00,fc,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,70,09,01,30,f1,18,22,2c,00,01,00,ae,
SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,
SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,
SPI : RX: aa,55,86,00,86,00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,
SPI : RX: 00,
SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8f,03,97,03,1b,04,00,04,00,04,00,04,00,fc,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,40,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b8,00,

SPI : TX: aa,55,80,00,80,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8f,03,97,03,1b,04,00,04,00,04,00,04,00,fc,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8f,03,97,03,1b,04,00,04,00,04,00,04,00,fc,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8f,03,97,03,1b,04,00,04,00,04,00,04,00,fc,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8f,03,97,03,1b,04,00,04,00,04,00,04,00,fc,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,78,09,01,30,f1,18,22,2c,00,01,00,a6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,87,00,87,00,

SPI : CLEAR_TO_SEND

[illegible]

SPI : RX: aa,55,48,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b0,00,

SPI : TX: aa,55,81,00,81,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

[illegible]

SPI : CLEAR TO SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8f,03,97,03,1c,04,00,04,00,04,00,04,00,fb,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8e,03,97,03,1c,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8e,03,97,03,1c,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8e,03,97,03,1c,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,40,09,01,30,f1,18,22,2c,00,01,00,9e,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,80,00,80,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,50,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,a8,00,

SPI : TX: aa,55,82,00,82,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8e,03,97,03,1c,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8e,03,97,03,1c,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8e,03,97,03,1c,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8e,03,97,03,1d,04,00,04,00,04,00,04,00,fb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,48,09,01,30,f1,18,22,2c,00,01,00,96,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,81,00,81,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,58,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,a0,00,

SPI : TX: aa,55,83,00,83,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8e,03,97,03,1d,04,00,04,00,04,00,04,00,fb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8e,03,97,03,1d,04,00,04,00,04,00,04,00,fb,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : TX: aa,55,84,00,84,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8d,03,96,03,1d,04,00,04,00,04,00,04,00,f9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8d,03,96,03,1d,04,00,04,00,04,00,04,00,f9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8d,03,96,03,1d,04,00,04,00,04,00,04,00,f9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,58,09,01,30,f1,18,22,2c,00,01,00,86,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,83,00,83,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8d,03,96,03,1e,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,68,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,90,00,

SPI : TX: aa,55,85,00,85,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8d,03,96,03,1e,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,09,01,7b,02,0d,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8d,03,96,03,1e,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8d,03,96,03,1e,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,60,09,01,30,f1,18,22,2c,00,01,00,be,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,84,00,84,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8d,03,96,03,1e,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,70,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,88,00,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8c,03,96,03,1e,04,00,04,00,04,00,04,00,fb,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,78,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,80,00,

SPI : TX: aa,55,87,00,87,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8c,03,96,03,1f,04,00,04,00,04,00,04,00,fa,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8c,03,96,03,1f,04,00,04,00,04,00,04,00,fa,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0b,01,7b,02,0f,04,00,00,02,00,06,00,26,b4,00,

[illegible]

SPI : CLEAR TO SEND

SPI : TX: aa.55.00.16.01.30.f1.18.21.1c.00.0e.02.8c.03.96.03.1f.04.00.04.00.04.00.04.00.fa.00.

[illegible]

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,70,09,01,30,f1,18,22,2c,00,01,00,ae,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,86,00,86,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8c,03,96,03,1f,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,40,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b8,00,

SPI : TX: aa,55,80,00,80,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8c,03,96,03,1f,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8c,03,95,03,1f,04,00,04,00,04,00,04,00,f9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8c,03,95,03,1f,04,00,04,00,04,00,04,00,f9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,78,09,01,30,f1,18,22,2c,00,01,00,a6,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,00,

SPI : RX: aa,55,87,00,87,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8c,03,96,03,1f,04,00,04,00,04,00,04,00,fa,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,48,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b0,00,

SPI : TX: aa,55,81,00,81,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8c,03,95,03,1f,04,00,04,00,04,00,04,00,f9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8c,03,95,03,1f,04,00,04,00,04,00,04,00,f9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8c,03,95,03,1f,04,00,04,00,04,00,04,00,f9,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,40,09,01,30,f1,18,22,2c,00,01,00,9e,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,80,00,80,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8b,03,95,03,1f,04,00,04,00,04,00,04,00,fe,00,

[illegible]

SPI : CLEAR_TO_SEND

[illegible]

SPI : RX: aa,55,50,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,a8,00,

SPI : TX: aa,55,82,00,82,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8b,03,95,03,20,04,00,04,00,04,00,04,00,c1,00,

[illegible]

SPI : CLEAR TO SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8b,03,95,03,20,04,00,04,00,04,00,04,00,c1,00,

[illegible]

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8b,03,95,03,20,04,00,04,00,04,00,04,00,c1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,48,09,01,30,f1,18,22,2c,00,01,00,96,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,81,00,81,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8b,03,95,03,20,04,00,04,00,04,00,04,00,c1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,58,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,a0,00,

SPI : TX: aa,55,83,00,83,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8b,03,95,03,20,04,00,04,00,04,00,04,00,c1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,09,01,7b,02,0d,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8b,03,95,03,20,04,00,04,00,04,00,04,00,c1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8b,03,95,03,20,04,00,04,00,04,00,04,00,c1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,50,09,01,30,f1,18,22,2c,00,01,00,8e,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,82,00,82,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,02,00,06,00,26,b4,00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,70,0d,01,30,f1,17,25,00,00,05,01,00,00,00,1c,97,

SPI : TX: aa,55,86,00,86,00,

SPI : RX: 00,00,00,00,00,00,

LC:State:AutoNode:Shutdown Request:Tout=28sec

LC:System:Shutdown request

LC:ev=15

SPI : TX: aa,55,60,0a,01,30,f1,17,27,0c,00,02,00,00,94,00,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,84,00,84,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0d,04,00,00,01,00,06,00,26,b4,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8b,03,95,03,21,04,00,04,00,04,00,04,00,c0,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0a,01,7b,02,0e,04,00,00,01,00,06,00,26,b7,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8b,03,95,03,21,04,00,04,00,04,00,04,00,c0,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8a,03,95,03,21,04,00,04,00,04,00,04,00,c1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0d,01,7b,02,12,04,00,00,02,00,06,00,26,af,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8a,03,94,03,21,04,00,04,00,04,00,04,00,c0,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

*FAN2PIC=0

w1= 0 y1= 0 e1= 0 pf8= 0 u1= 0

SPI : TX: aa,55,70,09,01,30,f1,18,22,2c,00,01,00,ae,

SPI : RX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : WAIT_FOR_ACK

SPI : TX: 00,00,00,00,00,00,

SPI : RX: aa,55,86,00,86,00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0d,01,7b,02,12,04,00,00,02,00,06,00,26,af,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8a,03,95,03,21,04,00,04,00,04,00,04,00,c1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: 00,

SPI : RX: aa,55,40,0c,01,30,f1,17,29,00,00,04,00,00,0f,01,b8,00,

SPI : TX: aa,55,80,00,80,00,

SPI : RX: 00,00,00,00,00,00,

LC:NOBS:AutoNode:OS Running

LC:ev=26

SPI : TX: 00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,00,

SPI : RX: aa,55,48,0d,01,30,f1,17,25,00,00,05,07,00,00,00,00,b5,

SPI : TX: aa,55,81,00,81,00,

SPI : RX: 00,00,00,00,00,00,

LC:ev=16

LC:State:AutoNode:Shutdown:Achieved

nRESET_PIC2SDARS=0 DIR=OUT, nRESET_PIC2FRONT=active

ENABLE_PIC2SDARS=0

SPI : TX: aa,55,00,16,01,30,f1,18,21,0c,00,0e,03,0d,01,7b,02,11,04,00,00,01,00,06,00,26,af,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

SPI : TX: aa,55,00,16,01,30,f1,18,21,1c,00,0e,02,8a,03,95,03,21,04,00,04,00,04,00,04,00,c1,00,

SPI : RX: 00,

SPI : CLEAR_TO_SEND

LC:ev=30

LC:State:AutoNode:SystemSleep

RTCounter at sleep=294

Jacinto OFF